

REMARKS

Claims 1-20 are currently pending. Claims 1, 2, and 7 stand rejected under 35 U.S.C. §102(b) as being anticipated by Dangelo et al. (U.S. Pat. No. 5,493,508, hereinafter "Dangelo"). Claims 3-6, 8, 9, 11, 15, 16, and 20 have been rejected under 35 U.S.C. §103(a) as being unpatentable over Dangelo in view of Zizzo (U.S. Pat. No. 6,578,174, hereinafter "Zizzo"). Claims 12, 14, and 18 have been rejected as being unpatentable over the combination of Dangelo and Zizzo in further view of Watanabe et al. (U.S. Pat. No. 6,157,947, hereinafter "Watanabe"). Applicant appreciates the indication that claims 10 and 17 are allowable, and requests reconsideration of claims 1-9, 11-16, and 18-20.

Claims 1, 2, and 7 stand rejected under 35 U.S.C. §102(b) as being anticipated by Dangelo. This reference describes "a methodology for generating structural descriptions of complex digital devices from high-level descriptions and specifications." Abstract. The Examiner has asserted that the methodology disclosed by Dangelo includes all the limitations found in claims 1, 2, and 7. However, as shown below, Dangelo fails to teach each and every element required in claims 1, 2, and 7.

Specifically, Dangelo fails to teach a method whereby a user is prompted "for a selection of a first memory compiler unit from a plurality of memory compiler units." Dangelo discloses a "Memory Compiler (MemComp) 602" that "takes high level specification for memory megacells and produces logic and layout files for the purpose of simulation, testing and layout." Col. 13, lns. 62-65. However, the memory controller is not one of a plurality of memory compilers that is selected for use by a prompted user. As such, Dangelo fails to anticipate that called for in claims 1, 2, and 7.

Although not clear, it appears that the Examiner has rejected independent claims 13 and 19 under the same basis in which claim 1 was rejected. Claim 13 calls for, in part, "prompting a user to select a memory compiler unit." As shown above, the methodology taught by Dangelo does not include the prompting of a user to select or otherwise choose a memory compiler. Accordingly, claim 13 is believed to be in condition for allowance.

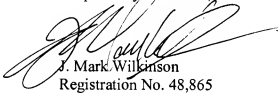
Claim 19 calls for, in part, "a system comprising a plurality of memory compiler units" and a combination datasheet that "comprises memory instances created by at least two of the

plurality of memory compiler units." As discussed above, Dangelo teaches a single memory compiler unit. As such, claim 19 is believed to be in condition for allowance.

Claims 3-6, 8, 9, 11, 12, 14-16, 18, and 20 depend and limit otherwise allowable claims. Their allowance is therefore also requested.

In conclusion, in light of at least the foregoing, claims 1-20 are believed to be in condition for allowance. A timely issuance of a Notice of Allowance for claims 1-20 is respectfully requested.

Respectfully submitted,




J. Mark Wilkinson
Registration No. 48,865

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HAYNES AND BOONE, LLP
901 Main Street, Suite 3100
Dallas, Texas 75202-3789
Telephone: 972/739-6927
Facsimile: 214/200-0853
Attorney Docket No.: 2003-0451/24061.78
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Diane Sutton